IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Name of Patentee: Lee A. Burton

Patent No.: 7,424,552 Issued: Sept. 9, 2008 Confirmation No. 4115

Title: SWITCH/NETWORK ADAPTER PORT INCORPORATING SHARED MEMORY RESOURCES SELECTIVELY ACCESSIBLE BY A DIRECT EXECUTION LOGIC ELEMENT AND ONE OR MORE

DENSE LOGIC DEVICES

ATTENTION: Certificate of Corrections Branch

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

REQUEST FOR CERTIFICATE OF CORRECTION OF PATENT FOR PTO Mistake (37 C.F.R. 1.322(a))

DEAR SIR:

An error appears in this patent. The error is a formatting mistake by the PTO. The error occurred in good faith. Correction thereof does not involve such changes in the patent as would constitute new matter or would require reexamination.

Please send the certificate to:

William J. Kubida Hogan & Hartson LLP One Tabor Center 1200 17th Street, Suite 1500 Denver, CO 80202

Although no fee is believed due, any fee deficiency associated with this transmittal may be charged to Deposit Account 50-1123.

Date: 0.3 A July n 2008

Respectfully submitted,

William J. Kubida, Reg. No. 29,664

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